EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	5	((HARTMUT) near2 (HENKEL)).INV.	US-PGPUB; USPAT	OR	ON	2010/10/28 11:59
L10	4	((MICHAEL) near2 (HEINEMANN)).INV.	US-PGPUB; USPAT	OR	ON	2010/10/28 11:59
L11	3	((JOCHEN) near2 (ZEUCH)).INV.	US-PGPUB; USPAT	OR	ON	2010/10/28 11:59
L12	6	9 10 11	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:00
L13	3053	307/64-66.ccls.	US-PGPUB; USPAT	OR	ON	2010/10/28 12:00
L14	69	L13 and (backup back? up battery) with pwm	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:00
L16	619887	@py="2010"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:00
L17	7	14 and 16	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:00
L18	54761	ups! uninterruptible	US-PGPUB; USPAT	OR	ON	2010/10/28 12:01
L19	217	L18 and (input with decoupl$4)	US-PGPUB; USPAT; USOCR	OR	ON	2010/10/28 12:01
L20	9	L19 and (decoupl$4 with (transistor switch)) and 16	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:01
L21	56981	L18 307/64-66.ccls.	US-PGPUB; USPAT	OR	ON	2010/10/28 12:02
L22	1734	L21 and (battery with current with (control $4 limit$4))	US-PGPUB; USPAT	OR	ON	2010/10/28 12:02
L23	161	L22 and 16	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:02
L25	9	23 and battery with pwm	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:03

L26	1868	307/64.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:04
L27	27	26 and battery with pwm	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:04
L28	8	27 and (current with transistor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:04
L29	31834	H02J001/00 H02J007/00 H02J009/00	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:07
L30	335	29 and battery with pwm	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:07
L31	107	30 and (current with transistor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:07
L32	10	30 and (current with source near3 transistor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2010/10/28 12:07

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1678	(ups! uninterruptible).clm.	USPAT; UPAD	OR	ON	2010/10/28 11:56
L2	22	L1 and ((monitor$4 sens $4) with current with (transistor source mosfet fet)).clm.	USPAT; UPAD	OR	ON	2010/10/28 11:56
L3	12	(pwm (pulse adj width adj modulat$4)).clm. and (monitor$4 with source).clm. and (control with gate).clm.	USPAT; UPAD	OR	ON	2010/10/28 11:56
L4	1	L3 and (ups! uninterruptible).clm.	USPAT; UPAD	OR	ON	2010/10/28 11:56
L5	1330	307/64.ccls.	USPAT; UPAD	OR	ON	2010/10/28 11:57
L6	1565	307/64.ccls.	US-PGPUB; USPAT; UPAD	OR	ON	2010/10/28 11:57

| L7 | 60 | 6 and (pwm (pulse adj width adj modulat$4)). clm. | US-PGPUB; USPAT; UPAD | OR | ON | 2010/10/28 11:57 |
| L8 | 24 | 7 and (current with (transistor source mosfet fet)).clm. | US-PGPUB; USPAT; UPAD | OR | ON | 2010/10/28 11:57 |

10/28/2010 12:08:04 PM
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